

FIG. 2A

FIG. 2B

FIG. 2C

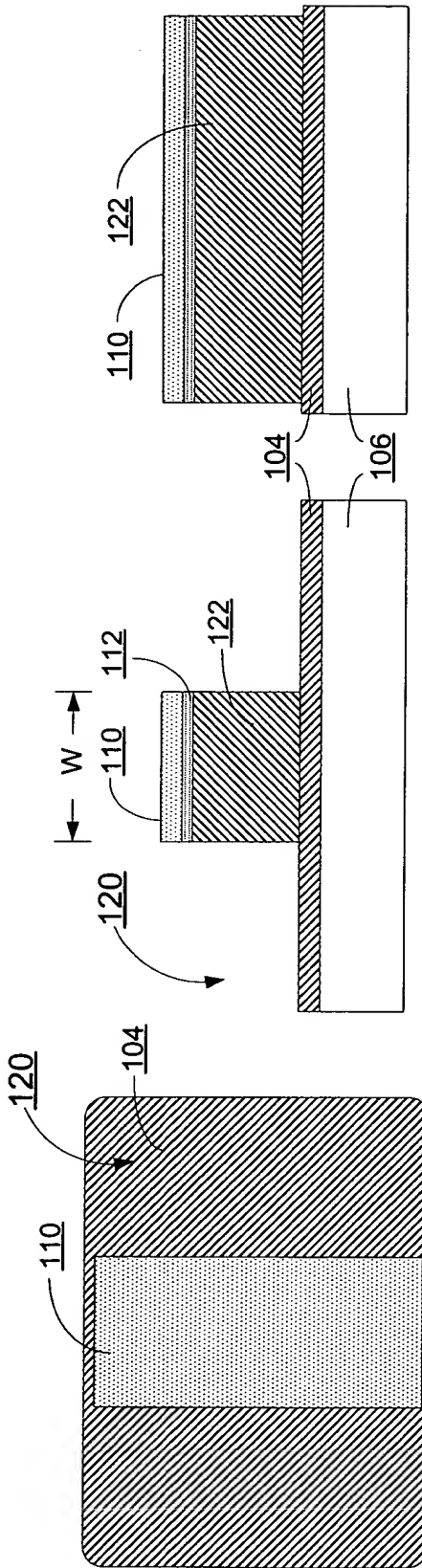


FIG. 3A

FIG. 3B

FIG. 3C

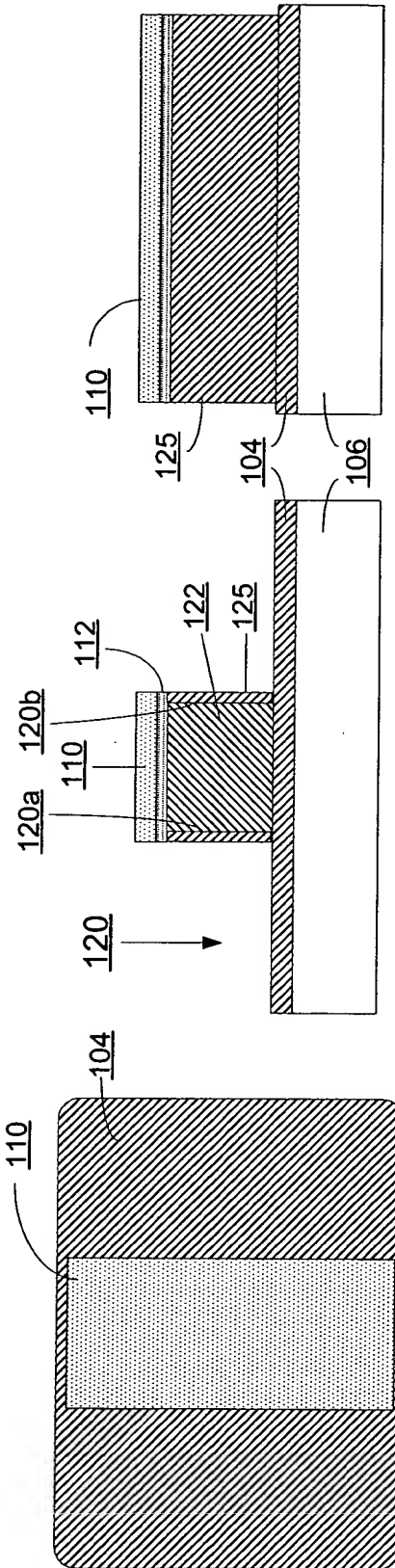


FIG. 4C

FIG. 4B

FIG. 4A

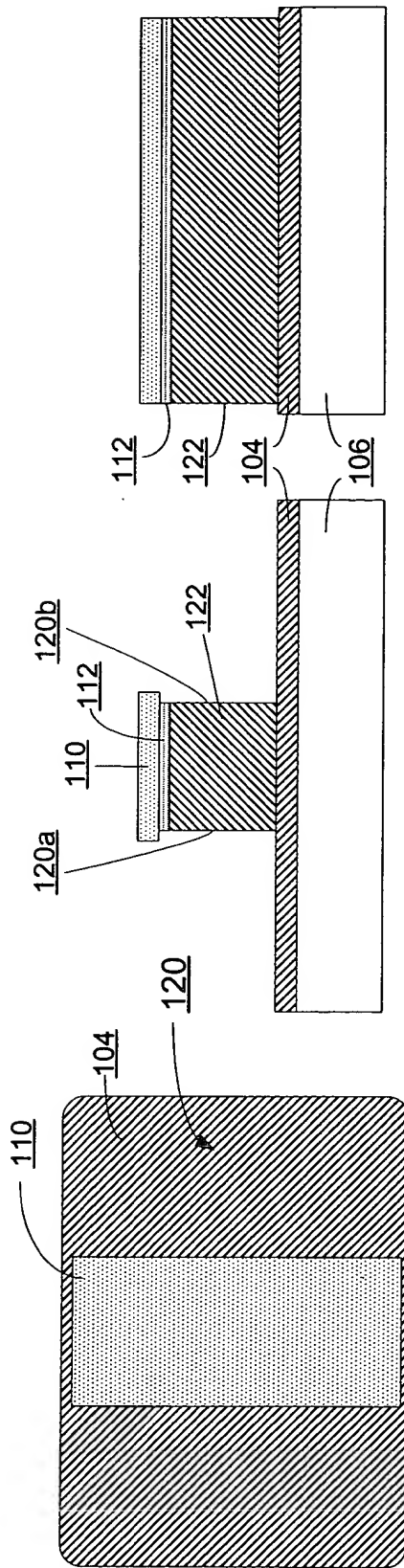


FIG. 5A

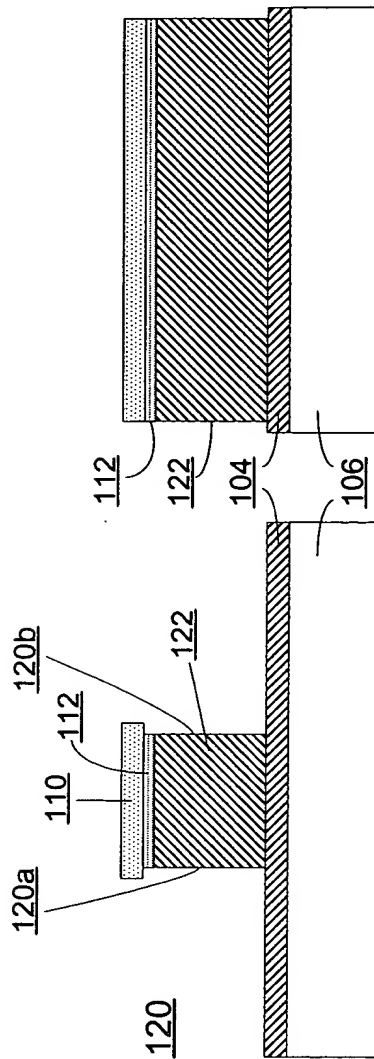


FIG. 5B

FIG. 5C

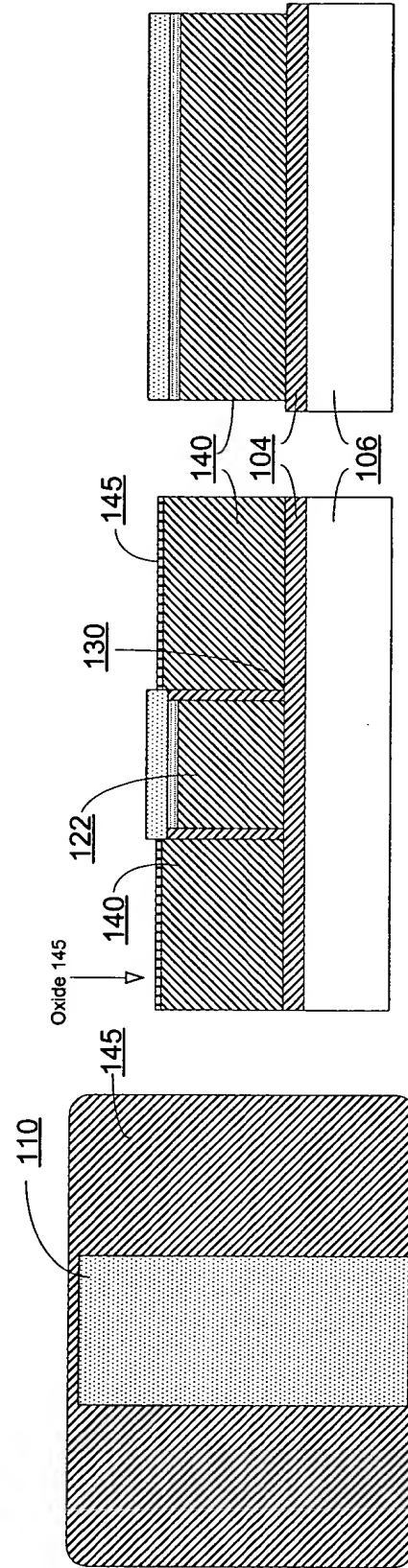


FIG. 6A

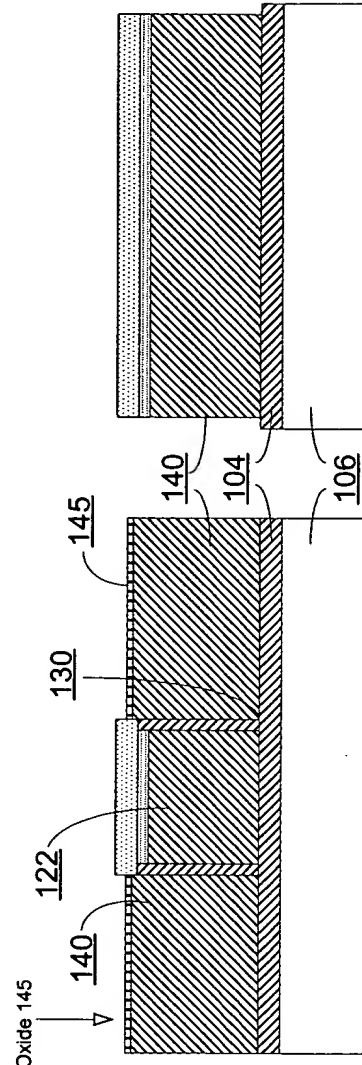


FIG. 6B

FIG. 6C

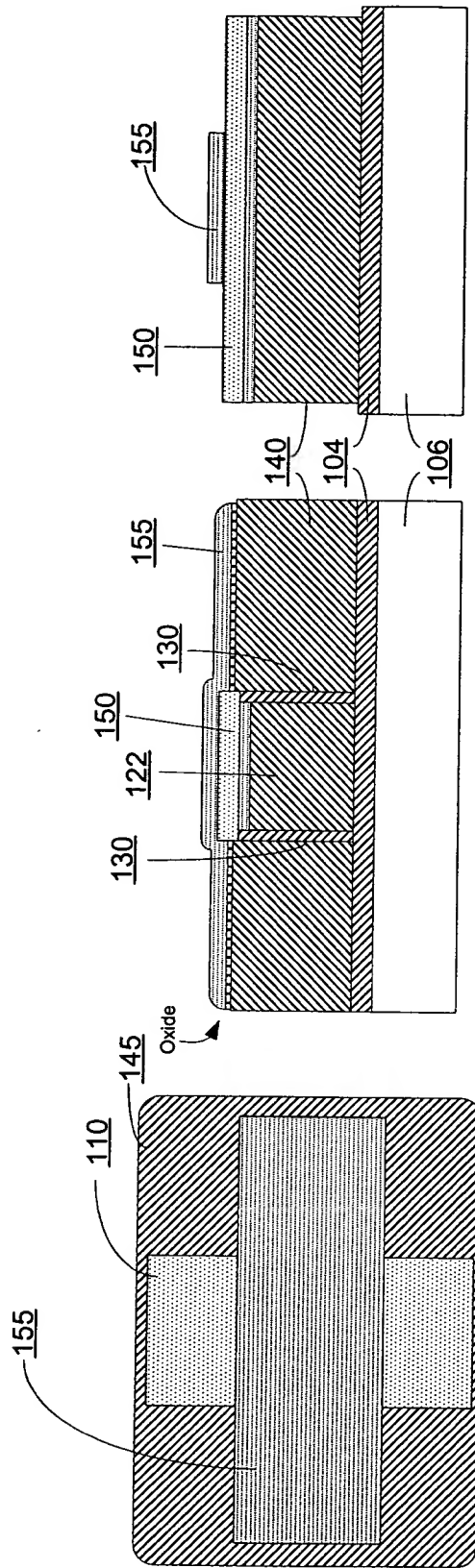


FIG. 7A

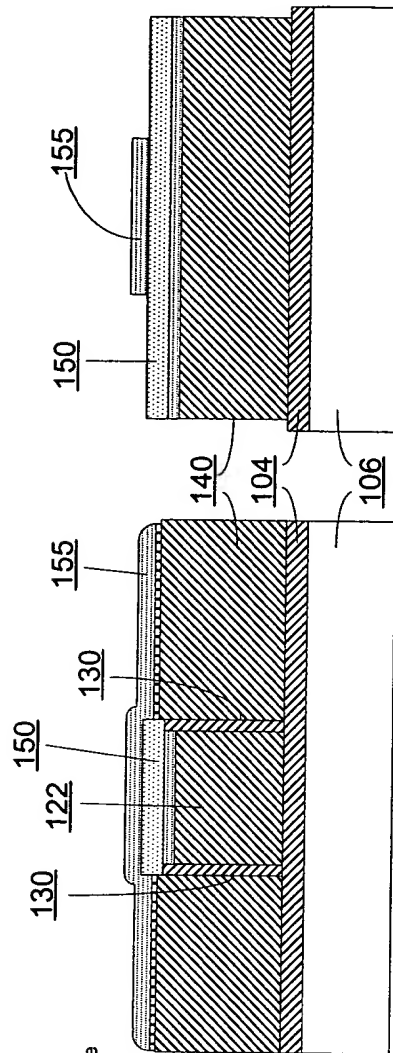


FIG. 7B

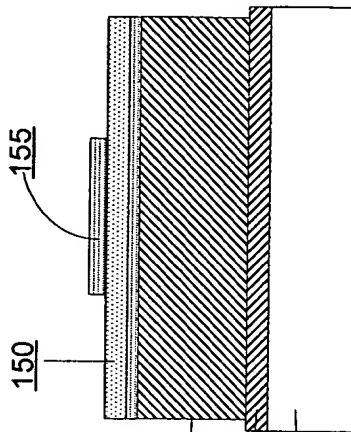


FIG. 7C

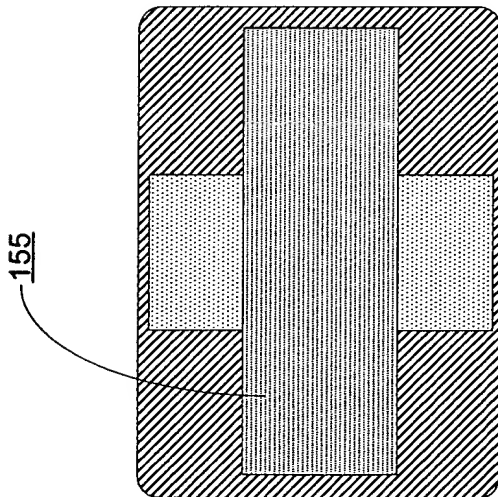


FIG. 8A

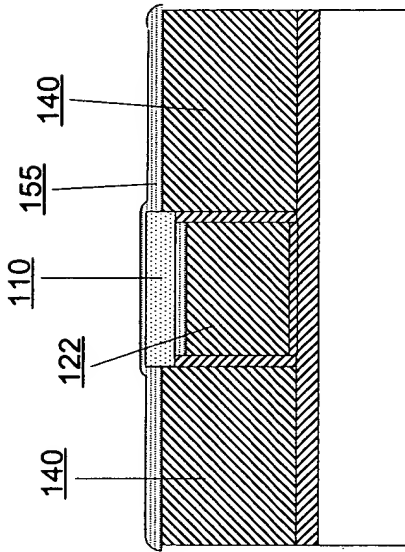


FIG. 8B

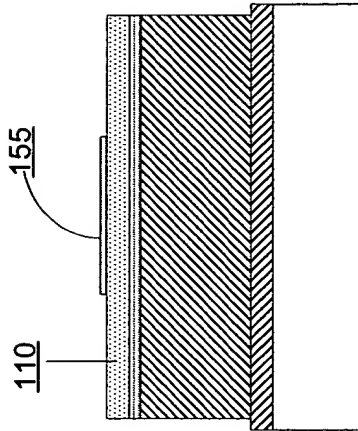


FIG. 8C

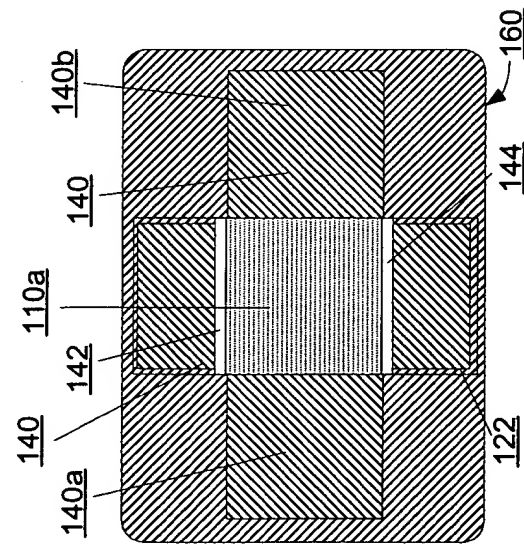


FIG. 9A

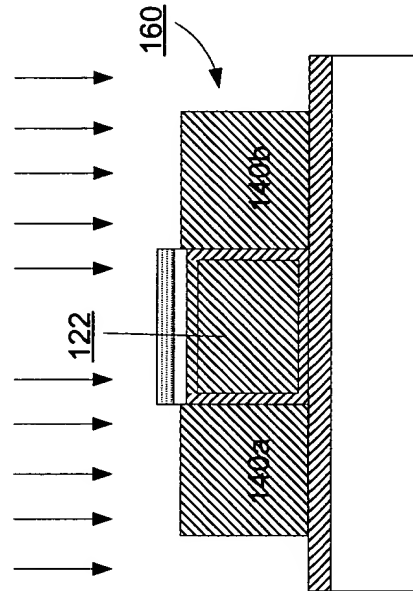


FIG. 9B

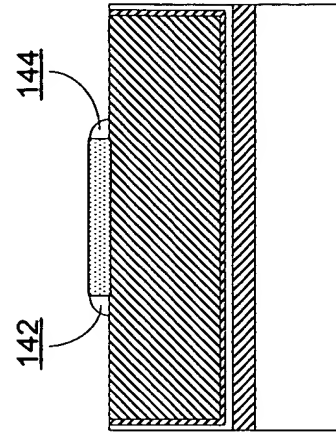


FIG. 9C

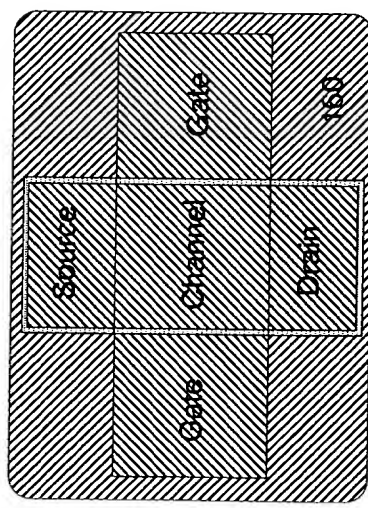


FIG. 10A

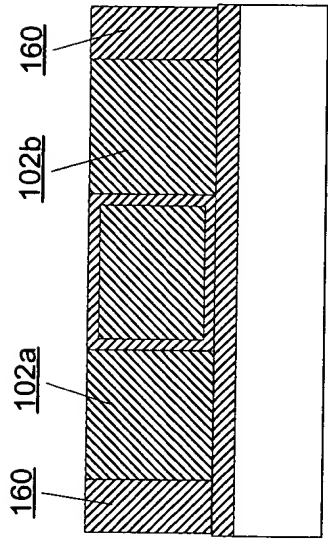


FIG. 10B

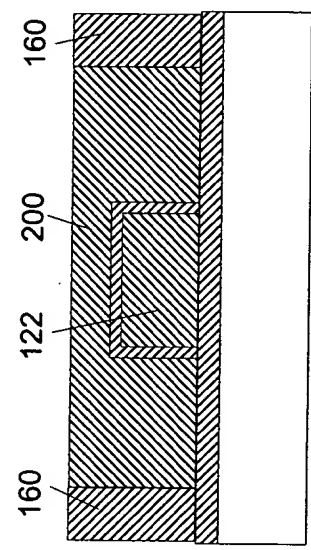


FIG. 11

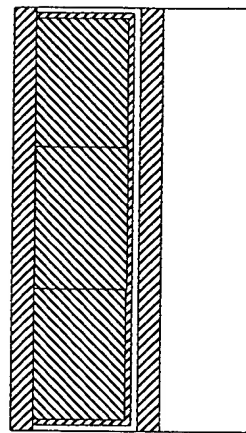


FIG. 10C

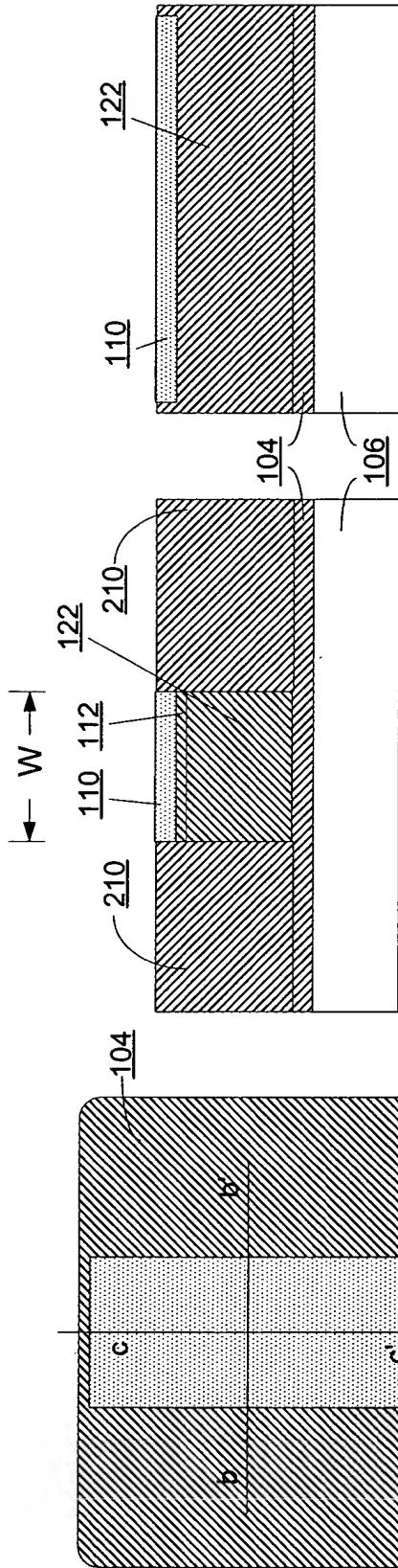


FIG. 12A

FIG. 12B

FIG. 12C

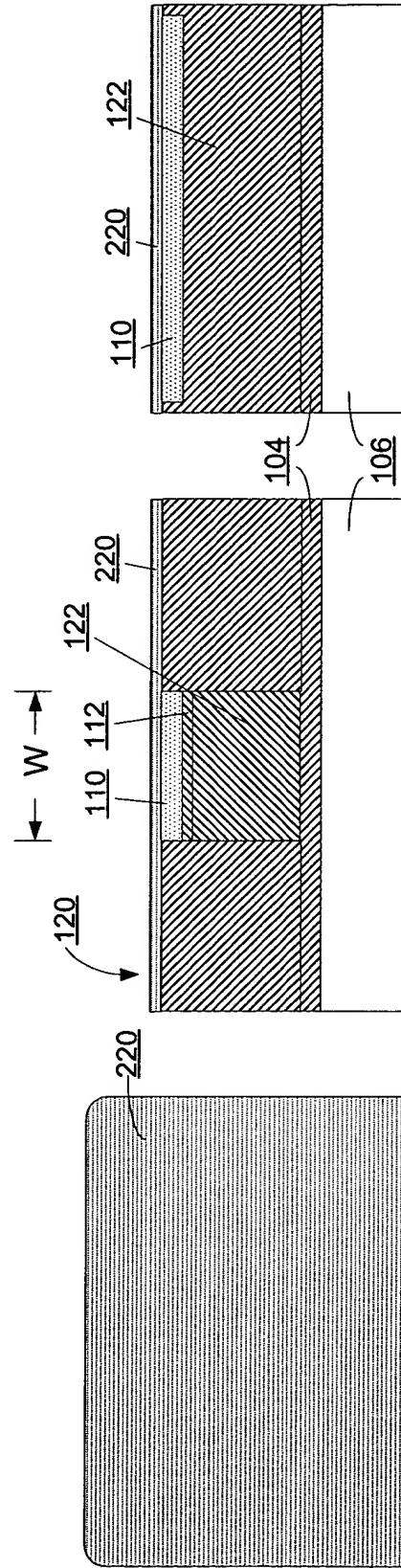


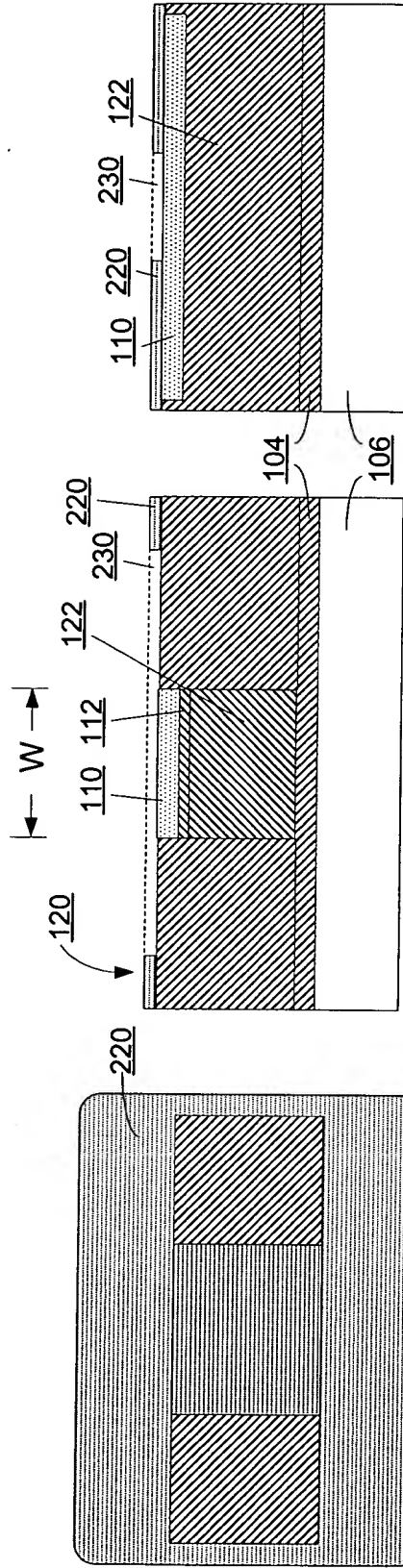
FIG. 13A

FIG. 13B

FIG. 13C

Title: SELF-ALIGNED TRIPLE GATE SILICON-ON-INSULATOR DEVICE
 Inventor: Krivokapic et al.
 App. No.: 09/711,445
 Docket No.: AMDI-08399US0
 Attorney: Stephen R. Bachmann

Filing Date: Novem 2000
 Art Unit: 2811
 Telephone No.: 415-362-3800



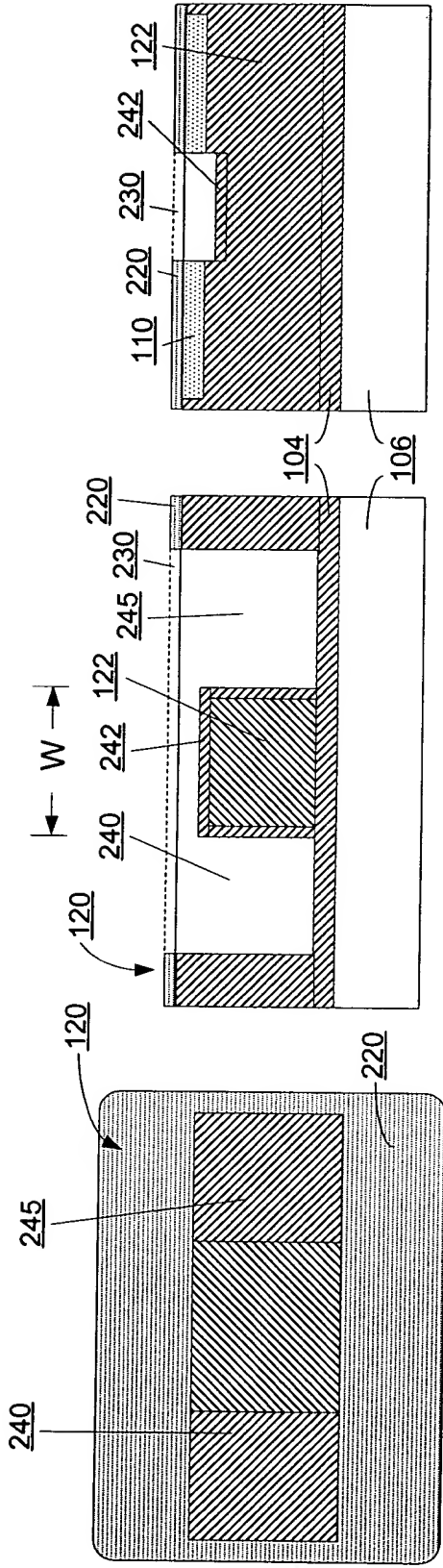


FIG. 15A

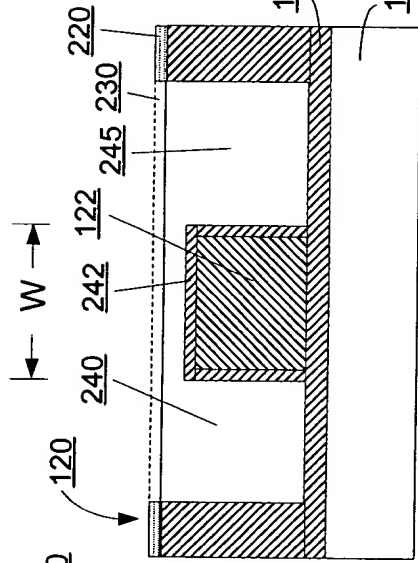


FIG. 15B

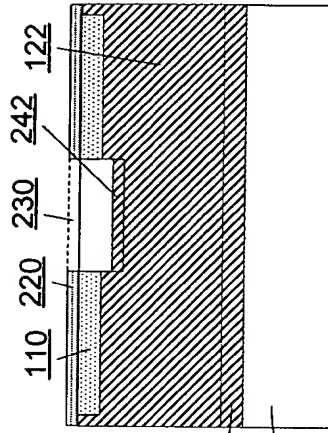


FIG. 15C

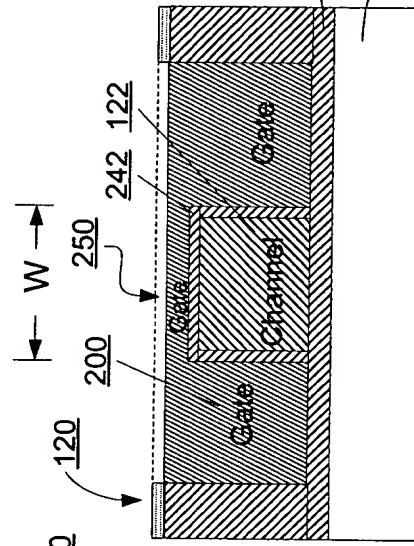


FIG. 16B

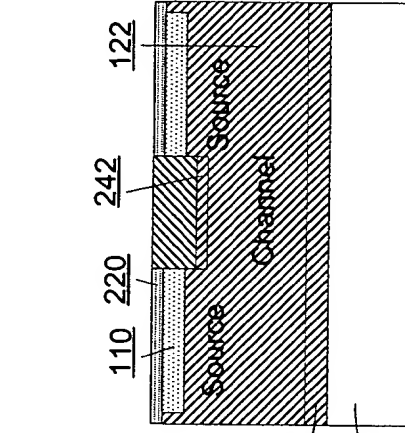


FIG. 16C

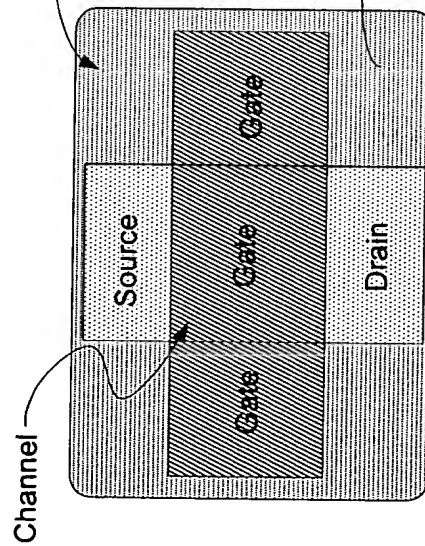


FIG. 16A